

Notice of References Cited	Application/Control No. 09/942,697		Applicant(s)/Patent Under Reexamination AMRHEIN ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,054,756	10-1977	Comella et al.	379/88.26
*	B	US-4,068,155	01-1978	Robbins et al.	318/568.1
*	C	US-4,227,253	10-1980	Ehrsam et al.	380/45
*	D	US-4,238,853	12-1980	Ehrsam et al.	380/45
*	E	US-4,242,730	12-1980	Golias et al.	356/39
*	F	US-4,276,594	06-1981	Morley, Richard E.	713/600
*	G	US-5,287,178	02-1994	Acampora et al.	348/384.1
*	H	US-5,305,277	04-1994	Derwin et al.	365/230.02
*	I	US-5,373,411	12-1994	Grass et al.	361/64
*	J	US-5,459,485	10-1995	Tomita et al.	345/589
*	K	US-5,565,360	10-1996	Lapota et al.	435/286.7
*	L	US-5,600,711	02-1997	Yuen, Henry C.	379/102.03
*	M	US-5,840,572	11-1998	Copeland et al.	435/286.7

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	Implementing real-time event channels on CAN-bus Kaiser, J.; Brudna, C.; Mitidieri, C.; Factory Communication Systems, 2004. Proceedings. 2004 IEEE International Workshop on 22-24 Sept. 2004 Page(s):247 - 256 Digital Object Identifier 10.1109/WFCS.2004.1377719			
*	V	The consumer electronic bus symbol encoding sublayer: a twisted pair implementation Douligeris, C.; Khawand, J.; Khawand, C.; Southeastcon '92, Proceedings., IEEE 12-15 April 1992 Page(s):385 - 388 vol.1 Digital Object Identifier 10.1109/SECON.1992.202375			
*	W	Enhancements to the time synchronization standard IEEE-1588 for a system of cascaded bridges Jasperneite, J.; Shehab, K.; Weber, K.; Factory Communication Systems, 2004. Proceedings. 2004 IEEE International Workshop on 22-24 Sept. 2004 Page(s):239 - 244 Digital Object Identifier 10.1109/WFCS.2004.1377716			
*	X	A physical layer implementation for a twisted pair home automation system Khawand, J.; Douligeris, C.; Khawand, C.; Consumer Electronics, IEEE Transactions on Volume 38, Issue 3, Aug 1992 Page(s):530 - 536 Digital Object Identifier 10.1109/30.156733			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 09/942,697		Applicant(s)/Patent Under Reexamination AMRHEIN ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,995,449	11-1999	Green et al.	367/83
*	B	US-6,122,756	09-2000	Baxter et al.	714/30
*	C	US-6,182,049	01-2001	Barker et al.	705/7
*	D	US-6,310,829	10-2001	Green et al.	367/82
*	E	US-6,450,258	09-2002	Green et al.	166/250.17
*	F	US-6,650,880	11-2003	Lee et al.	455/259
*	G	US-6,704,617	03-2004	Cherfane et al.	700/239
*	H	US-6,763,883	07-2004	Green et al.	166/250.17
*	I	US-7,146,519	12-2006	Barr et al.	713/500
*	J	US-7,149,913	12-2006	Barr et al.	713/500
*	K	US-7,206,960	04-2007	Barr et al.	713/600
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Implementing real-time event channels on CAN-bus Kaiser, J.; Brudna, C.; Mitidieri, C.; Factory Communication Systems, 2004. Proceedings. 2004 IEEE International Workshop on 22-24 Sept. 2004 Page(s):247 - 256 Digital Object Identifier 10.1109/WFCS.2004.1377719
*	V	Automatic Layout of Silicon-On-Silicon Hybrid Packages Preas, B.; Pedram, M.; Curry, D.; Design Automation, 1989. 26th Conference on 25-29 June 1989 Page(s):394 - 399
*	W	The consumer electronic bus symbol encoding sublayer: a twisted pair implementation Douligeris, C.; Khawand, J.; Khawand, C.; Southeastcon '92, Proceedings., IEEE 12-15 April 1992 Page(s):385 - 388 vol.1 Digital Object Identifier 10.1109/SECON.1992.202375
*	X	Monitoring of a Class of Timed Discrete Events Systems Allahham, A.; Alia, H.; Robotics and Automation, 2007 IEEE International Conference on 10-14 April 2007 Page(s):1003 - 1008 Digital Object Identifier 10.1109/ROBOT.2007.363116

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 09/942,697	Applicant(s)/Patent Under Reexamination AMRHEIN ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	TITAC-2: an asynchronous 32-bit microprocessor Takamura, A.; Imai, M.; Ozawa, M.; Fukasaku, I.; Fujii, T.; Kuwako, M.; Ueno, Y.; Nanya, T.; Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia and South Pacific 10-13 Feb. 1998 Page(s):319 - 320 Digital Object Identifier 10.1109/ASPDAC.1998.669480
*	V	Fault tolerant quantum cellular array (QCA) design using triple modular redundancy with shifted operands Wei, T.; Wu, K.; Karri, R.; Orailoglu, A.; Design Automation Conference, 2005. Proceedings of the ASP-DAC 2005. Asia and South Pacific Volume 2, 18-21 Jan. 2005 Page(s):1192 - 1195 Vol. 2 Digital Object Identifier 10.1109/ASPDAC.2005.1466555
*	W	Scaling deeper to submicron: on-line testing to the rescue Nicolaidis, M.; Zorian, Y.; Design, Automation and Test in Europe Conference and Exhibition 1999. Proceedings 9-12 March 1999 Page(s):432 Digital Object Identifier 10.1109/DATE.1999.761161
*	X	Switch-level timing models in the MOS simulator BRASIL Warmers, H.; Sass, D.; Horneber, E.-H.; Design Automation Conference, 1990. EDAC. Proceedings of the European 12-15 March 1990 Page(s):568 - 572 Digital Object Identifier 10.1109/EDAC.1990.136711

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 09/942,697		Applicant(s)/Patent Under Reexamination AMRHEIN ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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	E	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Discrete-time actuator fault estimation design for flight control application Jiong-Sang Yee; Bin Jiang; Jian Liang Wang; Yan Li; Control, Automation, Robotics and Vision, 2002. ICARCV 2002. 7th International Conference on Volume 3, 2-5 Dec. 2002 Page(s):1287 - 1292 vol.3
*	V	Design of Dynamic System Fault-Tolerant Control using IMM Estimation and RBF Neural Network Xudong Wang; Symos, V.L.; Control and Automation, 2006. MED '06. 14th Mediterranean Conference on June 2006 Page(s):1 - 6 Digital Object Identifier 10.1109/MED.2006.328822
*	W	Structural control for overlapping interconnected power systems Xue-Bo Chen; Stankovic, S.S.; Control and Automation, 2005. ICCA '05. International Conference on Volume 1, 26-29 June 2005 Page(s):391 - 396 Vol. 1 Digital Object Identifier 10.1109/ICCA.2005.1528151
*	X	Industrial processes susceptibility in relation to their environment Sudria, A.; Galceran, S.; Yeste, G.; Rull, J.; Bergas, J.; Benitez, I.; Vazquez, L.; Emerging Technologies and Factory Automation, 2001. Proceedings. 2001 8th IEEE International Conference on 15-18 Oct. 2001 Page(s):629 - 634 vol.1 Digital Object Identifier 10.1109/ETFA.2001.9964

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	Fault Diagnosis Method for Mobile Robots Using Multi-CMAC Neural Networks Liu, Yutian; Jiang, Jingping; Automation and Logistics, 2007 IEEE International Conference on 18-21 Aug. 2007 Page(s):903 - 907 Digital Object Identifier 10.1109/ICAL.2007.4338694			
*	V	Active Fault-tolerant Control Design for a Class of Time-delay Systems Fuqiang You; Zuohua Tian; Songjiao Shi; Intelligent Control and Automation, 2006. WCICA 2006. The Sixth World Congress on Volume 2, 21-23 June 2006 Page(s):5535 - 5538 Digital Object Identifier 10.1109/WCICA.2006.1714132			
*	W	High speed peer-to-peer communications based bus protection Apostolov, A.P.; Power Engineering Society Winter Meeting, 2001. IEEE Volume 2, 28 Jan.-1 Feb. 2001 Page(s):693 - 698 vol.2 Digital Object Identifier 10.1109/PESW.2001.916938			
*	X	A computer-aided technique for generating substation interlocking schemes Sachdev, M.S.; Dhakal, P.; Sidhu, T.S.; Power Delivery, IEEE Transactions on Volume 15, Issue 2, April 2000 Page(s):538 - 544 Digital Object Identifier 10.1109/61.852981			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.